



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

USA Patent Application
Dr. Frank Sattler, et al
Serial No.: 09/617,450
Filed: July 17, 2000

METHOD FOR MODULATING A BASIC CLOCK
SIGNAL FOR DIGITAL CIRCUITS AND CLOCK
MODULATOR FOR IMPLEMENTING THE METHOD

Examiner: Young T. Tse
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S I R :

A M E N D M E N T

In response to the Office Action dated May 20, 2004, please amend
this application as follows:

Amendments to the specification begin on page 2 of this paper.

Amendments to the claims are reflected in the listing of claims
which begins on page 9 of this paper.

Remarks/Arguments begin on page 11 of this paper.